

Proof complexity, Dagstuhl  
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# **Provability of weak circuit lower bounds**

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based on a joint work with Moritz Müller

# Constructive proofs of circuit lower bounds

**Known circuit lower bounds** for  $f$  given explicitly:  $AC^0$ ,  $AC^0[p]$ , etc.  
very constructive:  $p$ -time algorithm often recognizing when  $f$  is hard  
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## Mathematical logic:

- **upper bounds:** Prove all **known circuit lower bounds** in a constructive mathematical theory, e.g.  $PV_1$  (p-time reasoning).
  - exhibit a structure of algorithms recognizing hard functions?
- **lower bounds:**  $PV_1 \not\vdash$  **strong circuit lower bounds**?
  - stronger 'natural proofs' barrier:  $P=NP$  consistent with  $PV_1$ ?
  - circuit lower bounds as hard tautologies witnessing  $NP \neq coNP$ ?

# Bounded arithmetic and propositional logic

**PV<sub>1</sub>**: first-order theory formalizing **p-time reasoning** (Cook '75)

**APC<sub>1</sub>**: formalizes **probabilistic p-time reasoning** (Jeřábek '07)

$$\text{APC}_1 := \text{PV}_1 + \text{"}\exists f \notin \text{SIZE}(2^{\epsilon n})\text{"}$$

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If **PV<sub>1</sub>**  $\vdash \forall x A(x)$  for a p-time predicate  $A$ , then tautologies expressing  $\forall x A(x)$  have p-size Extended Frege **EF** proofs

If **APC<sub>1</sub>**  $\vdash \forall x A(x)$  for a p-time predicate  $A$ , then tautologies expressing  $\forall x A(x)$  have p-size **WF** proofs

**WF**: EF +  $\text{"}\exists f \notin \text{SIZE}(2^{\epsilon n})\text{"}$  (Jeřábek '04)

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where  $n_0, k$  are fixed constants

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$LB_{tt}(f, n^k)$ :

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Easier to reason about  $LB_{tt}(f, n^k)$  than about  $LB(f, n^k)$ .

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$$tt(f, n^k): \quad \bigvee_{y \in \{0,1\}^n} f(y) \neq C(y) \quad (\text{expresses } LB_{tt}(f, n^k))$$

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Possible witnessing  $w$  of  $LB(f, n^k)$ : a p-time algorithm with

**input:** circuit  $C$  of size  $n^k$

**output:**  $y$  s.t.  $C(y) \neq f(y)$



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Fact: If  $tt(f, n^k)$  has no poly-size constant-depth Frege proofs, then  $lb_A(f, n^k)$  has no poly-size (full) Frege proofs.

# Previous results

## Lower bounds:

**Razborov:**  $S_2^2(\alpha) \not\vdash$  “ $LB_{tt}(SAT, n^k)$ ” unless cryptography breaks

**P.:**  $VNC^1 \not\vdash LB(SAT, n^k)$  unless  $SIZE(n^k) \subseteq_{approx}$  “subexp  $NC^1$ ”

**Krajíček-Oliveira:**  $\forall k \exists f \in P$  s.t.  $PV_1 \not\vdash f \in SIZE(cn^k)$

**Buss:**  $PV_1 \not\vdash NP = coNP$  unless  $P=NP$

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**Razborov-Krajíček:** Propositional systems with **feasible interpolation** property have no  $p$ -size proofs of  $tt(f, n^k)$  unless cryptography breaks.

**Raz:** **Resolution** has no  $p$ -size proofs of  $tt(f, n^k)$  (unconditionally).

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$tt(f, n^k)$  considered as candidate hard tautologies for EF.

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**Razborov:**  $PV_1 \vdash LB_{tt}(PARITY, AC^0(n^k))$

-  $AC^0(n^k)$ : constant depth circuits of size  $n^k$

$PV_1 \vdash LB_{tt}(MOD_q, AC^0[p](n^k))$  for  $p, q$  distinct primes

-  $AC^0[p](n^k)$ :  $AC^0(n^k)$  with  $mod_p$  gates

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**Krajíček:**  $APC_1 \vdash LB(PARITY, AC^0(n^k))$



# Complexity theory formalizable in $PV_1$ and $APC_1$

Theory	Theorem
$PV_1$	Cook-Levin's theorem the PCP theorem Hardness amplification ...
$APC_1$	$AC^0$ lower bounds $AC^0[p]$ lower bounds (with $2^{\log^{O(1)} n} \in Log$ ) Monotone circuit lower bounds Nisan-Wigderson's derandomization Impagliazzo-Wigderson's derandomization Goldreich-Levin's theorem Natural proofs barrier ...

Table: A list of formalizations.

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- standard proof using Jeřábek's machinery of approximate counting
- $\Pr[A] > p$ , for  $A \subseteq 2^n$ , witnessed by a  $p$ -time surjection  $s : A \mapsto p2^n$
- size of each set approximated by sampling  $poly(n)$  elements  
e.g. there are  $poly(n)$  restrictions  $\rho_1, \dots, \rho_t, t \leq poly(n)$  s.t.  
each  $n^k$ -size  $d$ -depth circuit is collapsed by some  $\rho \in \{\rho_1, \dots, \rho_t\}$   
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- standard proof with  $p$ -time surjections witnessing probabilities

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To get WF proofs of  $lb_A(f, AC^0[p](n^k))$  formulas (unconditionally) we give a **succinct naturalization** of Razborov-Smolensky's  $AC^0[p]$  lower bound.

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To predict  $f(x_{k+1})$  prove an  $s$ -size circuit lower bound (for  $\epsilon \in \{0, 1\}$ )

$$\bigvee_{i=1, \dots, k} C(x_i) \neq f(x_i) \vee C(x_{k+1}) \neq \epsilon$$

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**Learning:** - given bits  $f(x_1), \dots, f(x_k)$  for  $k$   $n$ -bit tuples  $x_1, \dots, x_k$   
- want to predict  $f(x_{k+1})$  on a new input  $x_{k+1} \in \{0, 1\}^n$

- o minimal circuit  $C$  computing  $f$  on  $x_1, \dots, x_k$  has to determine  $f(x_{k+1})$
- o say that the size of the minimal circuit  $C$  is  $s$

To predict  $f(x_{k+1})$  prove an  $s$ -size circuit lower bound (for  $\epsilon \in \{0, 1\}$ )

$$\bigvee_{i=1, \dots, k} C(x_i) \neq f(x_i) \vee C(x_{k+1}) \neq \epsilon$$

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A more sophisticated connection between circuit lower bounds and learning algorithms recently demonstrated by Carmosino et al.

## Naturalization / automatizability

- want a p-time algorithm which given  $lb_A(f, n^k)$  finds its proof if it exists
  - i.e. **succinct natural proof**
- 

**Learning:** - given bits  $f(x_1), \dots, f(x_k)$  for  $k$   $n$ -bit tuples  $x_1, \dots, x_k$

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Theorem: quasipolynomial-time algorithm generating WF proofs of  $lb_A(f, AC^0[p](n^k))$  for many functions  $f$ .

# Problems

- Derandomize known circuit lower bounds, i.e. prove them inside  $PV_1$ .  
1st step: Derandomize witnessing of known circuit lower bounds.
- Prove  $APC_1 \vdash LB(MOD_q, AC^0[p](n^k))$  without  $\exists m, |m| = 2^{\log^{O(1)} n}$ .
- $V^0 \vdash LB(PARITY, AC^0(n^k))$ ?

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Thank You for Your Attention